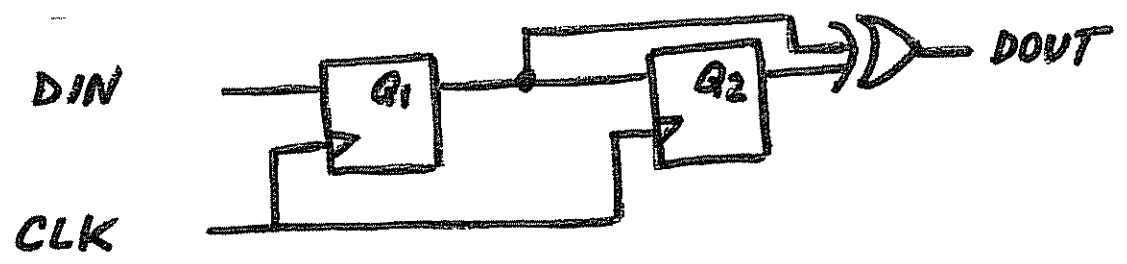
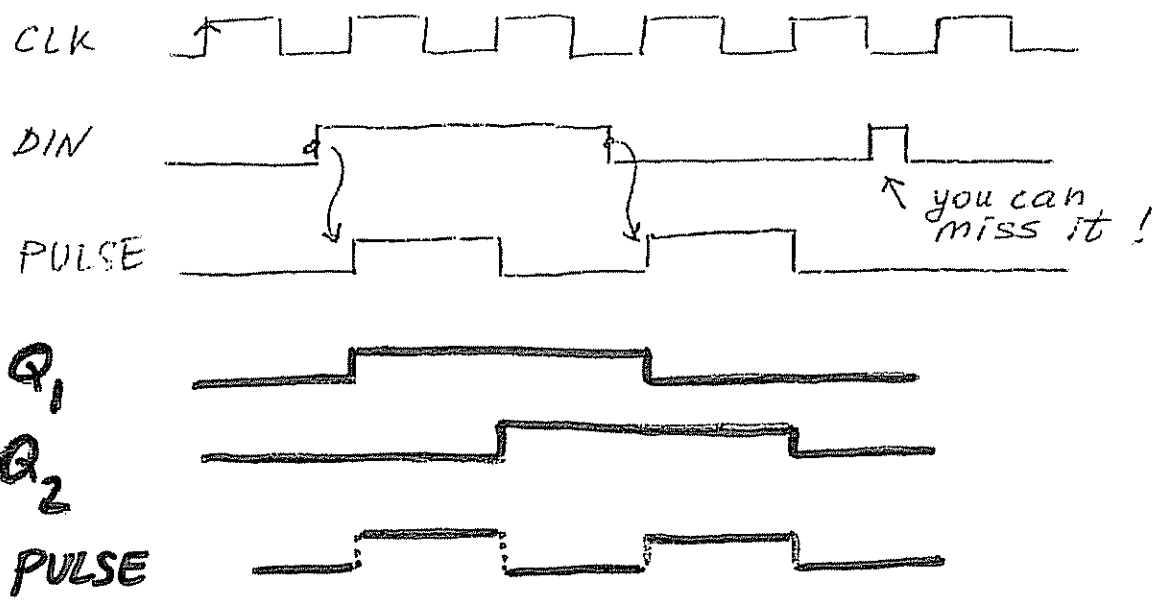
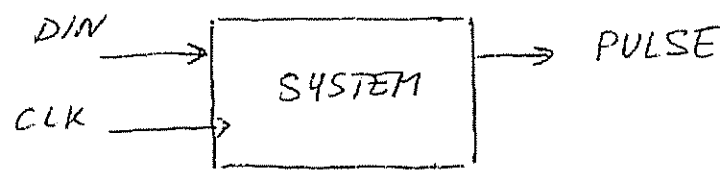
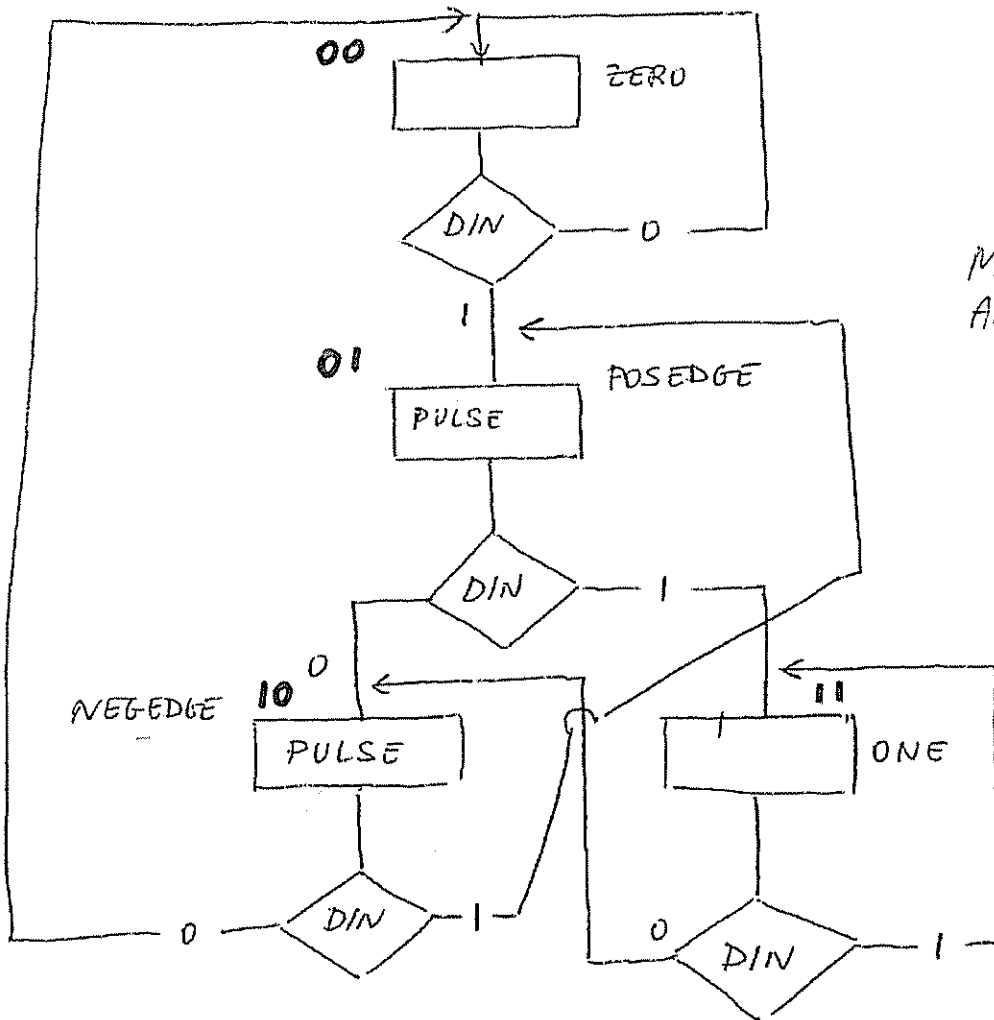
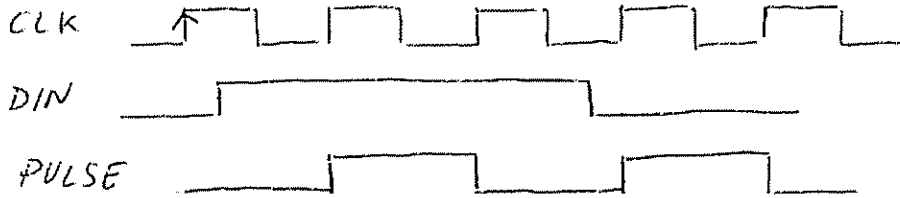
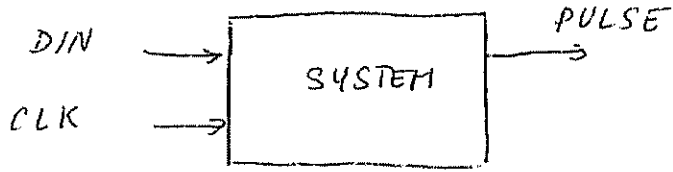


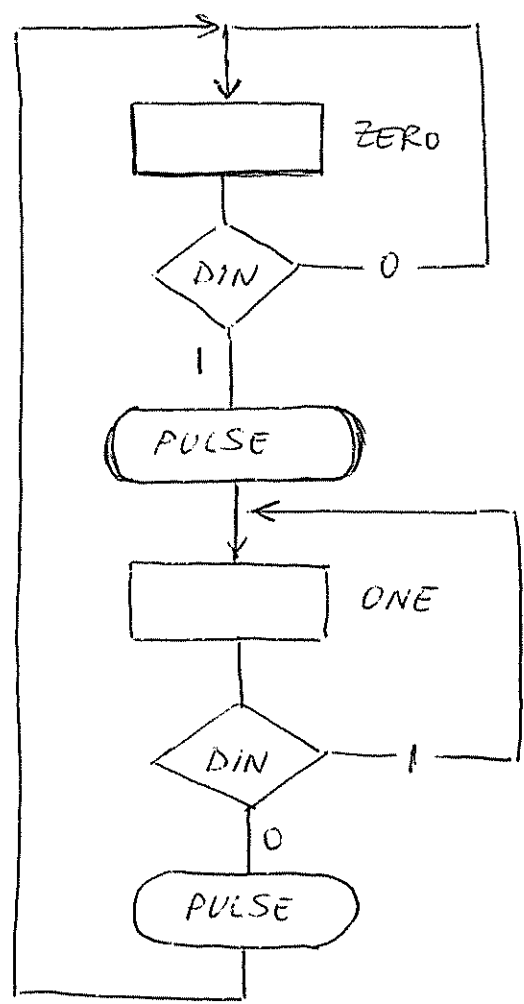
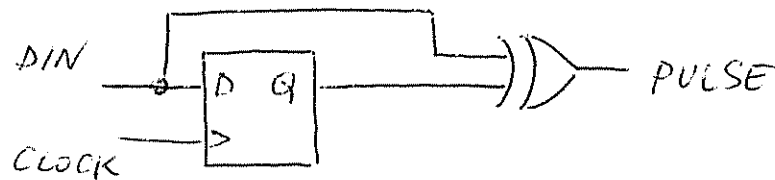
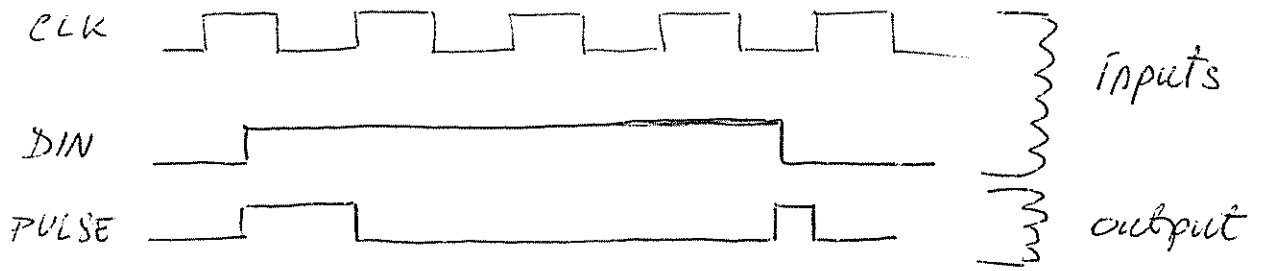
Exercise #1

Design a state machine to implement an edge detector.





MOORE
ASM



MEALY
ASM